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CONFIRMATION NO. 9561

SERIAL NUMBER 10/549,698	FILING OR 371(c) DATE 07/14/2006 RULE	CLASS 118	GROUP ART UNIT 1709	ATTORNEY DOCKET NO. 2342-0142PUS1	
APPLICANTS Masanori Sakai, Takaoka-shi, JAPAN; Toru Kagaya, Toyama-ken, JAPAN; Hirohisa Yamazaki, Toyama-shi, JAPAN;					
** CONTINUING DATA ***** This application is a 371 of PCT/JP04/09820 07/09/2004 <i>ke</i>					
** FOREIGN APPLICATIONS ***** JAPAN 2003-293953 08/15/2003 <i>ke</i>					
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 08/04/2006					
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Verified and Acknowledged <i>[Signature]</i> <i>KE</i> Examiner's Signature Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 5	TOTAL CLAIMS 10	INDEPENDENT CLAIMS 3
ADDRESS 2292					
TITLE Substrate treatment apparatus and method of manufacturing semiconductor device					
FILING FEE RECEIVED 1030	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		